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DIALOG(R) File 351:Derwent WPI
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012859058 **Image available**

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Image block distortion reduction circuit for video image decoder -
applies low pass filtering to image data of attention pixel located at
image block boundary, along determined correlation direction of attention
pixel with surrounding pixel

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Patent Family:

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JP 11298898	A	19991029	JP 98122810	A	19980416	200003 B

Priority Applications (No Type Date): JP 98122810 A 19980416

Patent Details:

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JP 11298898	A	5	H04N-007/30	

Abstract (Basic): JP 11298898 A

NOVELTY - Based on direction of smallest amplitude change of image data corresponding to attention pixel and surrounding pixels, correlation value and direction between pixels is determined. If correlation value is less than specific level and attention pixel at boundary of image block, low pass filtering is applied to attention pixel image, along correlation direction.

USE - For video image decoder.

ADVANTAGE - Block distortion due to diagonal frequency component is prevented effectively without fading of image by application of filtering process to attention pixel image data long direction of correlation. DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of the image decoder.

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Derwent Class: W02; W03; W04

International Patent Class (Main): H04N-007/30

International Patent Class (Additional): H04N-001/409; H04N-001/41;
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